

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. YOSHIDA et al.
Serial No.: 08/865,864
Filed: May 30, 1997
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND PROCESS FOR MANUFACTURING THE SAME
Group: 2812
Examiner: J. Hack

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents October 13, 1999
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified
application as follows:

IN THE SPECIFICATION

Page 1, at the top of the page, insert the following:

-- CROSS-REFERENCE TO RELATED APPLICATION

This application is a divisional of application
Serial No. 08/865,864, filed May 30, 1997, the
entire disclosure of which is hereby incorporated
by reference. --

Page 1, line 13, delete "a" and insert --the--;

line 16, delete "Summary" and insert --Background--;

line 18, delete "is" and insert --has--, delete
"by", delete "of a" and insert --in that the--;